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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10066182	01/31/2002	365	201	2818	PHAN

**APPLICANTS: McClure David;

**CONTINUING DATA VERIFIED: NO

** FOREIGN APPLICATIONS VERIFIED: NO

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		01-C-050
Verified and Acknowledged Examiners's initials TP		

TITLE : Circuit and method for testing a ferroelectric memory device

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G. YES
ISSUE FEE		PREPARED FOR DRAWING	
Amount Due	Date Paid	Sheets Drawn	Fig. Drawn
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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